

# UNITED STATES PATENT AND TRADEMARK OFFICE



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/647,284	08/26/2003	Takato Handa .	60188-636	2659
	7590 05/19/2004		EXAMINER	
Jack Q. Leve McDERMOTT	r, Jr. Γ, WILL & EMERY		BREWSTER,	WILLIAM M
600 Thirteenth	Street, N.W.		ART UNIT	PAPER NUMBER
Washington, I	DC 20005-3096		2823	<b>.</b>
	**		DATE MAILED: 05/19/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

	Applicati n No.	Applicant(s)	
	10/647,284	HANDA ET AL.	
Office Action Summary	Examiner	Art Unit	
	William M. Brewster	2823	m
The MAILING DATE f this communication a Period for Reply	ppears on the cover sheet w	vith the correspondence addres	SS
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, at at 1 If NO period for reply is specified above, the maximum statutory perions after the reply within the set or extended period for reply will, by state than three months after the main earned patent term adjustment. See 37 CFR 1.704(b).	1.136(a). In no event, however, may a eply within the statutory minimum of this dwill apply and will expire SIX (6) MO tute. cause the application to become A.	reply be timely filed  rty (30) days will be considered timely.  NTHS from the mailing date of this commu	inication.
Status			
1) Responsive to communication(s) filed on 26	August 2003.		
	nis action is non-final.		
3)☐ Since this application is in condition for allow	ance except for formal mat	ters, prosecution as to the me	rits is
closed in accordance with the practice under			
Disposition of Claims			
		*	
4) Claim(s) <u>1-9</u> is/are pending in the application			
4a) Of the above claim(s) is/are withdr	awn from consideration.		
5) Claim(s) is/are allowed.	•	9 7	
6)⊠ Claim(s) <u>1,2 and 4</u> is/are rejected. 7)⊠ Claim(s) <u>3 and 5-9</u> is/are objected to.		*	
8)☐ Claim(s) are subject to restriction and	/or election requirement		
are subject to restriction and	or election requirement.		
Application Papers	ŧ		•
9)⊠ The specification is objected to by the Examin	ner.	*	
10) The drawing(s) filed on is/are: a) □ ac	ccepted or b) objected to	by the Examiner.	
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the corre	ection is required if the drawing	(s) is objected to. See 37 CFR 1.	.121(d).
11)☐ The oath or declaration is objected to by the I	Examiner. Note the attache	d Office Action or form PTO-1	52.
Priority under 35 U.S.C. § 119	,		
12)⊠ Acknowledgment is made of a claim for foreig a)⊠ All b)□ Some * c)□ None of:	gn priority under 35 U.S.C. {	§ 119(a)-(d) or (f).	
1. ☑ Certified copies of the priority document	nte have been received		
2. Certified copies of the priority document		unnlication No	
3. Copies of the certified copies of the pri			10
application from the International Bure		received in this ivational Stay	je
* See the attached detailed Office action for a lis		received	
		,	
	•		
Attachment(s)			
1) Notice of References Cited (PTO-892)		Summary (PTO-413)	
<ul> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date <u>082603</u>.</li> </ul>	Paper No(s	s)/Mail Date nformal Patent Application (PTO-152)	·
S. Patent and Trademark Office PTOL-326 (Rev. 1-04) Office A	Action Summary	Part of Paper No./Mail Date	051404

Art Unit: 2823

#### **DETAILED ACTION**

## Specification

A substitute specification excluding the claims is required pursuant to 37 CFR 1.125(a) because of a sampling of the following errors:

- p. 11, line 25, word after "0.15" is illegible,
- p. 12, line 20, "layer51" should have a space to separate the two words,
- p. 13, line 13, second word is illegible.

A substitute specification filed under 37 CFR 1.125(a) must only contain subject matter from the original specification and any previously entered amendment under 37 CFR 1.121. If the substitute specification contains additional subject matter not of record, the substitute specification must be filed under 37 CFR 1.125(b) and (c)

## Claim Objections

Claims 1, 8 are objected to because of the following informalities:

claim 1, line 3, replace "gateeletctode" with --gate electrode-
claim 8, line 10, replace "thesemiconductor" with --the semiconductor-
Appropriate correction is required.

Application/Control Number: 10/647,284

Art Unit: 2823

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 2, 4 are rejected under 35 U.S.C. 102(b) as being anticipated by Lin et al., U.S. Patent No. 5,518,941.

Lin anticipates a method for fabricating a semiconductor device, the method comprising the steps of:

- a) in fig. 4, forming a gate insulating film 32 and a gate electrode 61 over a first transistor region defined in a semiconductor substrate 20;
- b) forming, on the semiconductor substrate, a hard mask 38 having an opening for exposing the first transistor region therein, after the step a) has been performed, wherein forming part of the masking layer is forming layer 70, col. 3, line 42 col. 4, line 2;

limitations from claim 4: wherein the hard mask is one out of a BPSG film, a PSG film and a silicon nitride film: silicon nitride, col. 3, lines 17-41; c) in fig. 5, implanting an impurity into the semiconductor substrate in the manner of large-angled-tilt ion implantation 52, using the gate electrode and the hard mask as a mask for ion implantation, col. 3, line 42 - col. 4, line 2; and

limitations from claim 2: wherein in the step b), the thickness of the hard mask and the width of the opening of the hard mask are defined such that

Application/Control Number: 10/647,284

Art Unit: 2823

the impurity reaches a region 42 under the gate electrode during the largeangle-tilt ion implantation in the step c);

d) in fig. 7, removing the hard mask, after the step c) has been performed, col. 4, lines 3-23.

## Allowable Subject Matter

Claims 3, 5-7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 8, lines 6-10 cannot be found in the prior art of record.

Claim 9 is objected to, as claim 8 is not in allowable form.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to William M. Brewster whose telephone number is 571-272-1854. The examiner can normally be reached on Full Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

illiam M. Brewster

Art Unit: 2823

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

14 May 2004

WB